# **Silicon Power Transistors**

The MJW21193 and MJW21194 utilize Perforated Emitter technology and are specifically designed for high power audio output, disk head positioners and linear applications.

#### Features

- Total Harmonic Distortion Characterized
- High DC Current Gain -

 $h_{FE} = 20 \text{ Min} @ I_C = 8 \text{ Adc}$ 

- Excellent Gain Linearity
- High SOA: 2.25 A, 80 V, 1 Second
- Pb–Free Packages are Available

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V <sub>CEO</sub>	250	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	400	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	5.0	Vdc
Collector-Emitter Voltage - 1.5 V	V <sub>CEX</sub>	400	Vdc
Collector Current – Continuous – Peak (Note 1)	Ι <sub>C</sub>	16 30	Adc
Base Current - Continuous	Ι <sub>Β</sub>	5.0	Adc
Total Power Dissipation @ T <sub>C</sub> = 25°C Derate Above 25°C	P <sub>D</sub>	200 1.43	W W/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	– 65 to +150	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	0.7	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\thetaJA}$	40	°C/W

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

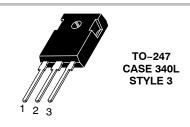
1. Pulse Test: Pulse Width = 5  $\mu$ s, Duty Cycle  $\leq$  10%.

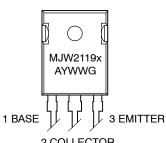


## **ON Semiconductor®**

http://onsemi.com

# **16 AMPERES** COMPLEMENTARY SILICON POWER TRANSISTORS 250 VOLTS, 200 WATTS





MARKING DIAGRAM

2 COLLECTOR

= 3 or 4

х

- = Assembly Location Α Υ
- = Year WW = Work Week
- = Pb-Free Package G

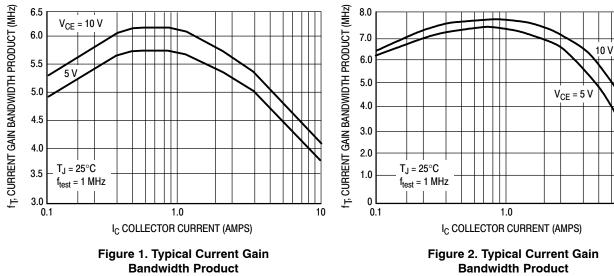
#### **ORDERING INFORMATION**

Device	Package	Shipping
MJW21193	TO-247	30 Units/Rail
MJW21193G	TO-247 (Pb-Free)	30 Units/Rail
MJW21194	TO-247	30 Units/Rail
MJW21194G	TO–247 (Pb–Free)	30 Units/Rail

#### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS				•	
Collector-Emitter Sustaining Voltage $(I_{C} = 100 \text{ mAdc}, I_{B} = 0)$	V <sub>CEO(sus)</sub>	250	-	_	Vdc
Collector Cutoff Current ( $V_{CE} = 200 \text{ Vdc}, I_B = 0$ )	I <sub>CEO</sub>	_	_	100	μAdc
Emitter Cutoff Current ( $V_{CE} = 5 \text{ Vdc}, I_C = 0$ )	I <sub>EBO</sub>	_	_	100	μAdc
Collector Cutoff Current (V <sub>CE</sub> = 250 Vdc, V <sub>BE(off)</sub> = 1.5 Vdc)	I <sub>CEX</sub>	_	_	100	μAdc
SECOND BREAKDOWN	• • • •				
Second Breakdown Collector Current with Base Forward B $(V_{CE} = 50 \text{ Vdc}, t = 1 \text{ s (non-repetitive)})$ $(V_{CE} = 80 \text{ Vdc}, t = 1 \text{ s (non-repetitive)})$	iased I <sub>S/b</sub>	4.0 2.25	-		Adc
DN CHARACTERISTICS					-
DC Current Gain ( $I_C = 8 \text{ Adc}, V_{CE} = 5 \text{ Vdc}$ ) ( $I_C = 16 \text{ Adc}, I_B = 5 \text{ Adc}$ )	h <sub>FE</sub>	20 8		80 -	
Base-Emitter On Voltage (I <sub>C</sub> = 8 Adc, V <sub>CE</sub> = 5 Vdc)	V <sub>BE(on)</sub>	_	_	2.2	Vdc
Collector-Emitter Saturation Voltage ( $I_C = 8 \text{ Adc}, I_B = 0.8 \text{ Adc}$ ) ( $I_C = 16 \text{ Adc}, I_B = 3.2 \text{ Adc}$ )	V <sub>CE(sat)</sub>			1.4 4	Vdc
DYNAMIC CHARACTERISTICS					
Total Harmonic Distortion at the Output V <sub>RMS</sub> = 28.3 V, f = 1 kHz, P <sub>LOAD</sub> = 100 W <sub>RMS</sub> h <sub>FE</sub>	T <sub>HD</sub>		0.8		%
(Matched pair $h_{FE} = 50 @ 5 A/5 V$ ) $h_{FE}$ matc		_	0.08	_	
Current Gain Bandwidth Product (I <sub>C</sub> = 1 Adc, V <sub>CE</sub> = 10 Vdc, f <sub>test</sub> = 1 MHz)	fT	4	-	-	MHz
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f <sub>test</sub> = 1 MHz)	C <sub>ob</sub>	_	_	500	pF





NPN MJW21194

10

#### **TYPICAL CHARACTERISTICS**

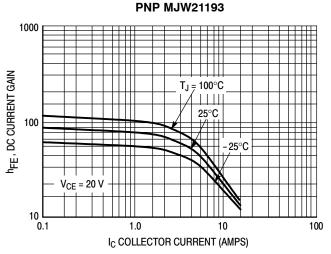
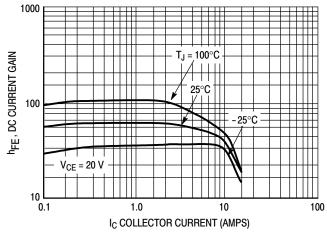


Figure 3. DC Current Gain, V<sub>CE</sub> = 20 V



NPN MJW21194

Figure 4. DC Current Gain, V<sub>CE</sub> = 20 V

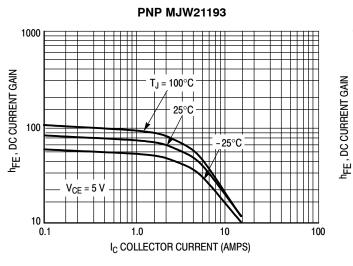
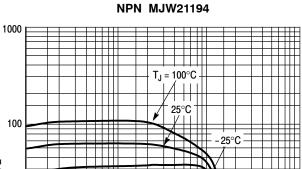
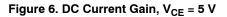


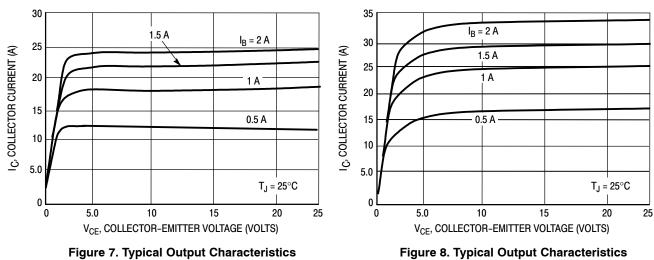
Figure 5. DC Current Gain, V<sub>CE</sub> = 5 V







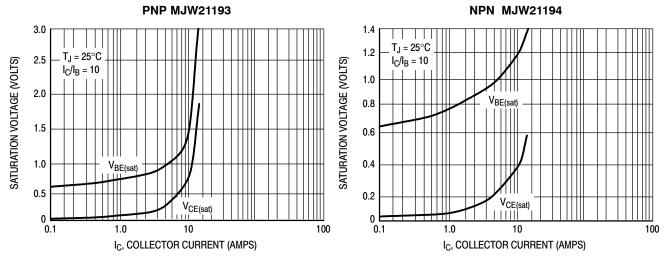
NPN MJW21194



10

PNP MJW21193

### **TYPICAL CHARACTERISTICS**







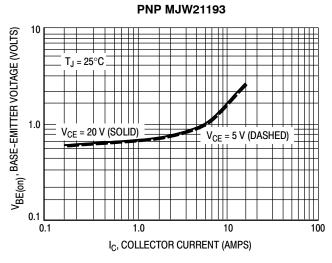


Figure 11. Typical Base–Emitter Voltage

1 Sec

10

100

10

1.0

0.1

1.0

I<sub>C</sub>, Collector Current (AMPS)

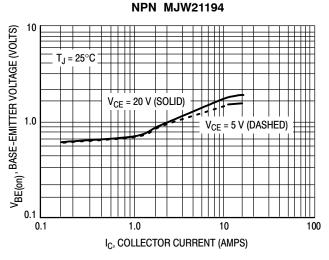
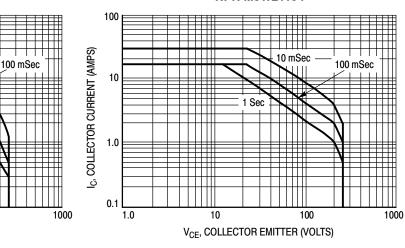


Figure 12. Typical Base–Emitter Voltage



PNP MJW21193

10 mSec

100



V<sub>CE</sub>, COLLECTOR EMITTER (VOLTS)

Figure 14. Active Region Safe Operating Area



There are two limitations on the power handling ability of a transistor; average junction temperature and secondary breakdown. Safe operating area curves indicate  $I_C - V_{CE}$  limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 13 is based on  $T_{J(pk)} = 150^{\circ}$ C;  $T_{C}$  is variable depending on conditions. At high case temperatures, thermal limitations will reduce the power than can be handled to values less than the limitations imposed by second breakdown.

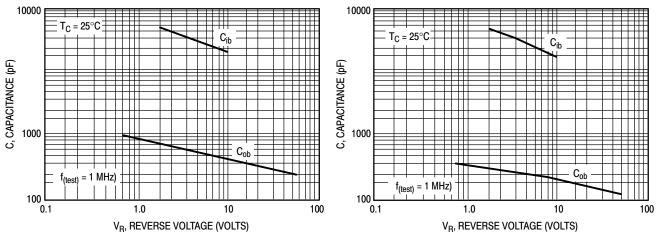


Figure 15. MJW21193 Typical Capacitance

Figure 16. MJW21194 Typical Capacitance

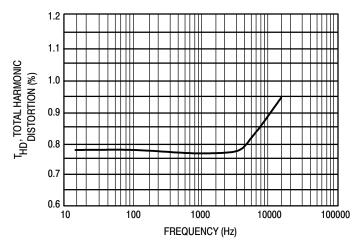


Figure 17. Typical Total Harmonic Distortion

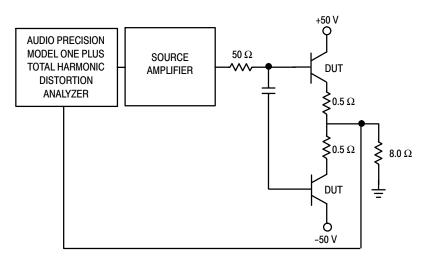
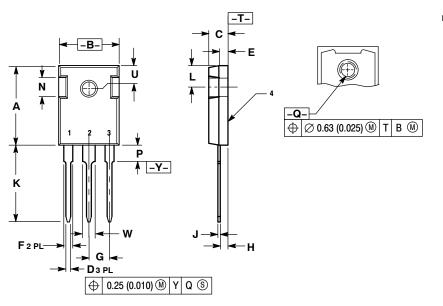


Figure 18. Total Harmonic Distortion Test Circuit

#### PACKAGE DIMENSIONS

**TO-247** CASE 340L-02 ISSUE E



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	20.32	21.08	0.800	8.30
В	15.75	16.26	0.620	0.640
С	4.70	5.30	0.185	0.209
D	1.00	1.40	0.040	0.055
Е	1.90	2.60	0.075	0.102
F	1.65	2.13	0.065	0.084
G	5.45 BSC		0.215 BSC	
Н	1.50	2.49	0.059	0.098
ſ	0.40	0.80	0.016	0.031
Κ	19.81	20.83	0.780	0.820
Г	5.40	6.20	0.212	0.244
Ν	4.32	5.49	0.170	0.216
Ρ		4.50		0.177
Q	3.55	3.65	0.140	0.144
C	6.15 BSC		0.242 BSC	
M	2.87	3.12	0.113	0.123

STYLE 3: PIN 1. BASE 2. COLLECTOR 3. EMITTER

ON Semiconductor and IIII are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death agsociated with such unintended or unauthorized use personal and solut LC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunit/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada Fax: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support: ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81–3–5773–3850

For additional information, please contact your local Sales Representative